

Tabulation of Marks : B. Tech. End-Term (regular) Semester 3 Examination - December 2023
Department of Electronics and Communication Engineering

SI No	Roll No	Name of Student	Course Code	EC3L01	EC3L02	EC3T01	EC3T02	EC3T03	EC3T04	ITB303	ITB312	MAT3T1	SGPA	No. of Back Papers	Result
			Course Name	Electronic Devices & Circuits Lab	Digital Electronics & Logic Design Lab	Network Theory	Electronic Devices & Circuits	Digital Electronics & Logic Design	Electrical Engineering Material	Data Structures	Data Structures Lab	Mathematics - III			
			Credit Hours	1.00	1.50	3.00	3.00	3.00	2.00	3.00	1.50	3.00			
1	2204248	MS. VIKHEIENU SOLEHO		A	B	O	B	C	A	A	C	B	8.36	0	PASSED
2	2204250	MR. ABHIJIT BAILUNG		B	C	O	D	E	B	D	O	DB	6.21	1	PROMOTED (with 1 C.P.)
3	2204251	MR. LONGLENG RINNGAMRIN		C	C	B	E	F	B	F	A	F	4.10	3	PROMOTED (with 5 C.P.)
4	2204252	MR. IGNATIUSH M. SANGMA		O	B	O	B	E	A	B	O	C	8.05	0	PASSED
5	2204253	MS. HABUNG RIYA		B	B	O	D	C	B	C	A	C	7.64	0	PASSED
6	2304266 (L)	MS. VINEINUO SOPFU		C	B	O	C	C	O	A	B	D	8.00	0	PASSED
7	2304267 (L)	MR. PEWANG T KONYAK		B	C	B	C	D	B	E	C	F	5.86	1	PROMOTED (with 1 C.P.)
8	2304268 (L)	MR. HACHIBEMO TSANGLAO		C	C	O	D	E	D	C	B	D	6.83	0	PASSED
9	2304269 (L)	MR. PUCITO LOHE		AB	AB	F	F	F	AB	F	AB	F	0.00	9	PROMOTED (with 9 C.P.)

C.P. - Carry Paper (includes previous semesters)

* Multiple Attempts

F - Failed

AB - Absent in Mid/End-Term

DB - Debarred

CH -

Cheating

WH - Withheld

Signature of Tabulators:

Verified by:

1. Mr. CHUMDEMO

1. Mr. SHANCHAMO YANTHAN.....

2. Mr. AKANGJUNGSHI LONGKUMER.....