

Tabulation of Marks : B. Tech. End-Term (regular) Semester 5 Examination - December 2023
Department of Electronics and Communication Engineering

SI No	Roll No	Name of Student	Course Code	EC5L01	EC5L02	EC5T01	EC5T02	EC5T03	EC5T04	EC5T05	EC5T06	SGPA	No. of Back Papers	Result
			Course Name	Microcontroller Lab	VHDL Lab	Antenna & Wave Propagation	Introduction to VHDL	Analog Communication	Microcontroller	Control Systems	Management & Entrepreneurship			
			Credit Hours	1.50	1.50	3.00	3.00	3.00	3.00	3.00	3.00			
1	2104232	MR. BOVIKA CHISHI		B	B	B	B	C	B	A	A	8.14	0	PASSED
2	2104233	MR. RICHARD JIEMPHONG		A	C	A	C	D	C	A	B	7.71	0	PASSED
3	2104235	MS. K AJILA YIM		B	A	B	A	B	C	A	A	8.36	0	PASSED
4	2104237	MR. HIMANKA PROTIM BORA		B	B	B	D	E	C	O	C	7.29	0	PASSED
5	2104238	MR. BEIZOLIE PFUSENUO		B	A	B	A	D	A	O	A	8.50	0	PASSED
6	2104240	MR. TEMSUNOK LKR		B	B	B	D	E	E	B	C	6.71	0	PROMOTED (with 1 C.P.)
7	2104241	MR. YEKIVI SHIKHU SWU		B	A	D	B	B	D	A	B	7.64	0	PASSED
8	2104242	MR. BURTONGZUK		B	B	C	A	A	C	O	A	8.43	0	PASSED
9	2104243	MR. MAITPHANG RANI		A	O	O	O	O	O	O	O	9.93	0	PASSED
10	2204256 (L)	MR. KABOTO T AWOMI		A	A	A	O	A	C	O	O	9.14	0	PASSED

C.P. - Carry Paper (includes previous semesters)

* Multiple Attempts
Cheating

F - Failed
WH - Withheld

AB - Absent in Mid/End-Term

DB - Debarred

CH -

Signature of Tabulators:

Verified by:

1. Mr. CHUMDEMO

1. Mr. SHANCHAMO YANTHAN.....

2. Mr. AKANGJUNGSHI LONGKUMER.....